03/12/04 10/8/10/2/18

				Docket Number (Optional) 50T5690.01	Application Number 10/800, 348 <u>Unassigned</u>					
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)									Applicant(s) Tenko Yamashita	
					Filing Date Filed Herewith		Group Art Unit Unassigned 2814			
					U.S. PAT	ENT DOCUMENTS				
*EXAMINER INITIAL	REF		DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING IF APPRO	
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	REF	UEF DOCUMENT NUMBER DATE		DATE	COUNTRY CLASS			SUBCLASS	Translation YES NO	
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								Date, Pertinent Pa		
TD			Momose, H.S. et al., "1.5 Devices, Vol. 50, No. 4, A	5- <i>nm Gate Oxide</i> April 2003, pp. 10	CMOS on 001-1008.	(110) Surface-Oriented Si .	Substrate,"	IEEE Transactio	ons On Ele	ectron
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

SILICON-ON INSULATOR (SOI) SUBSTRATE HAVING **DUAL SURFACE CRYSTALLOGRAPHIC ORIENTATIONS** AND METHOD OF FORMING SAME

Application Number:

10/800348

Confirmation Number:

9885

First Named Applicant:

Tenko Yamashita

Attorney Docket Number: 50T5690.01

Art Unit:

28124

Search string:

(5841170 or 6391692 or 6476445 or 6503813 or 6559035 or 6566712 or 6593173 or 6627505

or 6649455).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
10	1	5841170	1998-11-24	Adan et al.		257	345
70	2	6391692	2002-05-21	Nakamura	B1	438	151
40	3	6476445	2002-11-05	Brown et al.	B1	257	347
M	4	6503813	2003-01-07	Koburger, III	B1	438	424
40	5	6559035	2003-05-06	Villa et al.	B2	438	481
10	6	6566712	2003-05-20	Hayashi et al.	B2	257	347
10	7	6593173	2003-07-15	Anc et al.	B1	438	149
70	8	6627505	2003-09-30	Adan	B2	438	311
T	9	6649455	2003-11-18	Murakami	B2	438	149

Signature

Examiner Name	Date			
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